

UNITED STATES PATENT AND TRADEMARK OFFICE  
APPLICANT Son K. Quan et al. GROUP ART UNIT: 2831  
APPLN. NO.: 09/928,737 EXAMINER: Hung V. Ngo  
FILED: August 13, 2001 CONFIRMATION No.: 7252  
TITLE: SEMICONDUCTOR PACKAGE AND METHOD THEREFOR

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Certificate of Submission

I hereby certify that this correspondence is being submitted to the U.S.P.T.O., Alexandria, VA.

Addressed per C.F.R. § 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.

Facsimile transmitted in accordance with C.F.R. § 1.6(d).

Submitted electronically via EFS in accordance with "Legal Framework for EFS Web".

May 6, 2010

Date of Submission

/Dee Matocha/

Signature

Dee Matocha

Printed Name of Person Signing Certificate

**AMENDMENT IN FURTHERANCE OF RCE UNDER 37 CFR 1.114**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants are herein filing a Request for Continued Examination under 37 CFR 1.114 of U.S. Serial No. 09/928,737 and request entrance of the following amendments in view of the remarks presented thereafter.

**FEES**

Applicants continue to authorize the charging of Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any fees owed in connection with this application, or credit Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any refunds.